

PLI-LESS CO-SIMULATION OF ISS-BASED VERIFICATION SYSTEMS IN HARDWARE SIMULATORS

Abstract

A method for providing verification for a simulation design, including obtaining the simulation design comprising a programming language interface system call, encoding a target of the programming language interface system call into the simulation design to obtain a first modified simulation design, modifying the programming language interface system call to reference the target in the first modified simulation design to obtain a second modified simulation design, and verifying the second modified simulation design using a simulation testbench.

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